

ABSTRACT OF THE DISCLOSURE

*for AS*

A cache data control system and method for a computer system in which in a memory read processing, a coherent controller issues an advanced speculative read request for (speculatively) reading data from a cache data section in advance to a cache data controller, before reading a cache tag from a cache tag section and conducting cache hit check. When a cache hit has occurred, the cache data controller returns the data subjected to speculative reading as response data at the time when the cache data controller has received a read request issued by the coherent controller.